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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/542,982	07/21/2005	Mark Thomas Johnson	NL 031051	5626
24737	7590	02/04/2008	EXAMINER	
PHILIPS INTELLECTUAL PROPERTY & STANDARDS			LESPERANCE, JEAN E	
P.O. BOX 3001			ART UNIT	PAPER NUMBER
BRIARCLIFF MANOR, NY 10510			2629	
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02/04/2008		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary	Application No.	Applicant(s)
	10/542,982	JOHNSON, MARK THOMAS
Examiner	Art Unit	
Jean E. Lesperance	2629	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 21 July 2005.

2a) This action is **FINAL**. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-22 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1,2,20 and 21 is/are rejected.

7) Claim(s) 3-19 and 22 is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.

10) The drawing(s) filed on 21 July 2005 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) All b) Some * c) None of:
1. Certified copies of the priority documents have been received.
2. Certified copies of the priority documents have been received in Application No. _____.
3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) Notice of References Cited (PTO-892)
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date 8/15/07.

4) Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____.
5) Notice of Informal Patent Application
6) Other: _____

DETAILED ACTION

1. The application filed July 21, 2005 is presented for examination and claims 1-22 are pending.

Specification

2. This application does not contain an abstract of the disclosure as required by 37 CFR 1.72(b). An abstract on a separate sheet is required.

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1, 2, 20, and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over USPN 6,897,843 by ("Ayres et al.")

Regarding claim 1, Ayres et al. teach a row driver (30) of Fig.1 for selecting all the pixels (10), a data driver Fig.1 (35) for supplying drive waveforms (VD) to the selected pixels (10) via data electrode (16), and a controller Fig.1 (40) for controlling the row driver (30) to select a group of lines of pixels (10) (see the waveforms of Fig.6a), a plurality of address lines 14 for each row and a single column line 16 for each column. A select line 76 is provided on each row to control select transistor 74 which connects column line 16 to TFTs 12, via a data line 77 (see Fig.11) wherein a selection through TFT (74) select a plurality of lines at the same time. The prior art does not teach specifically select a group of lines of pixels at the same time. However, the prior art

teaches a plurality of address lines 14 for each row and a single column line 16 for each column. A select line 76 is provided on each row to control select transistor 74 which connects column line 16 to TFTs 12, via a data line 77 (see Fig.11) wherein a selection through TFT (74) select a plurality of lines at the same time. Thus, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify a plurality of address lines 14 for each row and a single column line 16 for each column. A select line 76 is provided on each row to control select transistor 74 which connects column line 16 to TFTs 12, via a data line 77 (see Fig.11) wherein a selection through TFT (74) select a plurality of lines at the same time to obtain select a group of lines of pixels at the same time because this would provide active matrix display devices, that offer or permit improvements over the known devices.

Regarding claim 2, Ayres et al. teach a controller Fig.1 (40) for controlling the row driver (30) to select a group of lines of pixels (10) (see the waveforms with a predetermined level, Fig.6a), a plurality of address lines 14 for each row and a single column line 16 for each column. A select line 76 is provided on each row to control select transistor 74 which connects column line 16 to TFTs 12, via a data line 77 (see Fig.11) wherein a selection through TFT (74) select a plurality of lines at the same time.

Regarding claim 20, Ayres et al. teach an electrophoretic and electroluminescent type display device (see Fig.1).

Regarding claim 21, Ayres et al. teach a row driver (30) of Fig.1 for selecting all the pixels (10), a data driver Fig.1 (35) for supplying drive waveforms (VD) to the selected pixels (10) via data electrode (16), and a controller Fig.1 (40) for controlling the

row driver (30) to select a group of lines of pixels (10) (see the waveforms of Fig.6a), a plurality of address lines 14 for each row and a single column line 16 for each column. A select line 76 is provided on each row to control select transistor 74 which connects column line 16 to TFTs 12, via a data line 77 (see Fig.11) wherein a selection through TFT (74) select a plurality of lines at the same time. The prior art does not teach specifically select a group of lines of pixels at the same time. However, the prior art teaches a plurality of address lines 14 for each row and a single column line 16 for each column. A select line 76 is provided on each row to control select transistor 74 which connects column line 16 to TFTs 12, via a data line 77 (see Fig.11) wherein a selection through TFT (74) select a plurality of lines at the same time. Thus, it would have been obvious to a person of ordinary skill in the art at the time the invention was made to modify a plurality of address lines 14 for each row and a single column line 16 for each column. A select line 76 is provided on each row to control select transistor 74 which connects column line 16 to TFTs 12, via a data line 77 (see Fig.11) wherein a selection through TFT (74) select a plurality of lines at the same time to obtain select a group of lines of pixels at the same time because this would provide active matrix display devices, that offer or permit improvements over the known devices.

Allowable Subject Matter

Claims 3-19 and 22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

4 Any inquiry concerning this communication or earlier communications from the
ably examiner should be directed to Jean Lesperance whose telephone number is (571)
272-7692. The examiner can normally be reached on from Monday to Friday between
10:00AM and 6:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's
supervisor, Richard Hjerpe, can be reached on (571) 272-7691.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks
Washington, D.C. 20231

or faxed to:

(571) 273-8300 (for Technology Center 2600 only)

Hand-delivered responses should be brought to Crystal Park II, 2121 Crystal
drive, Arlington, VA, Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or
proceeding should be directed to the technology Center 2600 Customer Service Office
whose telephone number is (703) 306-0377.

Jean Lesperance

Art Unit 2629

Date 1/30/2008



RICHARD HJERPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600